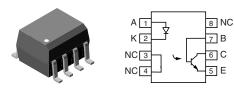


Vishay Semiconductors

Optocoupler, Phototransistor Output, with Base Connection in SOIC-8 Package



DESCRIPTION

The VO211AT, VO212AT, VO213AT are optically coupled pairs with a gallium arsenide infrared LED and silicon NPN phototransistor. Signal information, including a DC level, can be transmitted by the device while maintaining a high degree of electrical isolation between input and output.

The VO211AT, VO212AT, VO213AT comes in a standard SOIC-8 small outline package for surface mounting which makes it ideally suited for high density applications with limited space.

FEATURES

- Isolation test voltage, 4000 V_{RMS}
- Lead (Pb)-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC





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AGENCY APPROVALS

- UL1577, file no. E52744 system code Y
- CUL file no. E52744, equivalent to CSA bulletin 5A
- DIN EN 60747-5-5 (VDE 0884) available with option 1

ORDER INFORMATION	
PART	REMARKS
VO211AT	CTR > 20 %, SOIC-8
VO212AT	CTR > 50 %, SOIC-8
VO213AT	CTR > 100 %, SOIC-8

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
INPUT							
Peak reverse voltage		V _R	6	V			
Peak forward current	1 μs, 300 pps	I _{FM}	1	A			
Forward continuous current		I _F	60	mA			
Power dissipation		P _{diss}	90	mW			
Derate linearly from 25 °C			1.2	mW/°C			
OUTPUT			•				
Collector emitter breakdown voltage		BV _{CEO}	30	V			
Emitter collector breakdown voltage		BV _{ECO}	7	V			
Collector base breakdown voltage		BV _{CBO}	70	V			
I _{Cmax. DC}		I _{Cmax. DC}	50	mA			
I _{Cmax.}	t < 1 ms	I _{Cmax} .	100	mA			
Power dissipation		P _{diss}	150	mW			
Derate linearly from 25 °C			2	mW/°C			
COUPLER	· ·						
Isolation test voltage		V _{ISO}	4000	V_{RMS}			
Total package dissipation	LED and detector	P _{tot}	240	mW			
Derate linearly from 25 °C			3.2	mW/°C			
Storage temperature		T _{stg}	- 40 to + 150	°C			
Operating temperature		T _{amb}	- 40 to + 100	°C			
Soldering time	at 260 °C	T _{sld}	10	S			

Note

 T_{amb} = 25 °C, unless otherwise specified.

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.



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ELECTRICAL CHARACTERISTCS									
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT									
Forward voltage	I _F = 10 mA		V_{F}		1.3	1.5	V		
Reverse current	V _R = 6 V		I _R		0.1	100	μΑ		
Capacitance	$V_R = 0 V$		Co		13		pF		
OUTPUT									
Collector emitter breakdown voltage	$I_C = 100 \mu A$		BV _{CEO}	30			V		
Emitter collector breakdown voltage	$I_E = 10 \mu A$		BV _{ECO}	7			V		
Collector base breakdown voltage	$I_C = 100 \mu A$		BV _{CBO}	100			V		
Collector base current			I _{CBO}			1	nA		
Emitter base current			I _{EBO}			1	nA		
Collector dark current	V _{CE} = 10 V		I _{CEO}		5	50	nA		
Collector emitter capacitance	$V_{CE} = 0 V$		C _{CE}		10		pF		
Saturation voltage, collector emitter	I _F = 10 mA		V _{CEsat}			0.4	V		
COUPLER									
Isolation test voltage	1 s		V _{ISO}	4000			V_{RMS}		

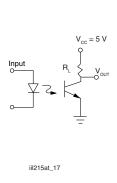
 T_{amb} = 25 °C, unless otherwise specified. Minimum and maximum values were tested requierements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
		VO211AT	CTR	20	50		%
Current transfer ratio	$I_F = 10 \text{ mA}, V_{CE} = 5 \text{ V}$	VO212AT	CTR	50	80		%
		VO213AT	CTR	100	130		%



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SWITCHING CHARACTERISTICS							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	I_C = 2 mA, R_L = 100 Ω , V_{CC} = 10 V		t _{on}		3		μs
Turn-off time	$I_C = 2 \text{ mA}, R_L = 100 \Omega, V_{CC} = 10 \text{ V}$		t _{off}		3		μs
Rise time	I_C = 2 mA, R_L = 100 Ω , V_{CC} = 10 V		t _r		3		μs
Fall time	$I_C = 2 \text{ mA}, R_L = 100 \Omega, V_{CC} = 10 \text{ V}$		t _f		2		μs



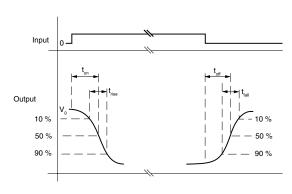
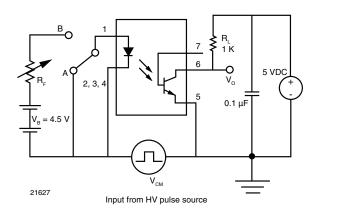


Fig. 1 - Switching Test Circuit

COMMON MODE TRANSIENT IMMUNITY								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Common mode transient immunity at logic high	$\begin{aligned} \text{V}_{\text{CM}} &= 1000 \text{ V}_{\text{P-P}}, \text{ R}_{\text{L}} = 1 \text{ k}\Omega, \\ \text{I}_{\text{F}} &= 0 \text{ mA} \end{aligned}$	C _{MH}		5000		V/µs		
Common mode transient immunity at logic low	$\begin{split} V_{CM} = 1000 \ V_{P\text{-}P}, \ R_L = 1 \ k\Omega, \\ I_F = 10 \ \text{mA} \end{split}$	C _{ML}		5000		V/µs		



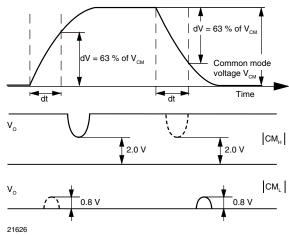


Fig. 2 - Test Circuit for Common Mode Transient Immunity



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SAFETY AND INSULATION RATINGS								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Climatic classification (according to IEC 68 part 1)				40/100/21				
Polution degree				2				
Comparative tracking index		CTI	175		399			
Isolation test voltage	1 s	V _{ISO}	4000			V _{RMS}		
Peak transient overvoltage		V _{IOTM}	6000			V		
Peak insulation voltage		V _{IORM}	560			V		
Resistance (input to output)		R _{IO}		100		GΩ		
Safety rating - power output		P _{SO}			350	mW		
Safety rating - input current		I _{SI}			150	mA		
Safety rating - temperature		T _{SI}			165	°C		
External creepage distance			4			mm		
External clearance distance			4			mm		
Internal creepage distance			3.3			mm		
Insulation thickness			0.2			mm		

Note

As per IEC 60747-5-2, § 7.4.3.8.1, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

TYPICAL CHARACTERISTICS

T_{amb} = 25 °C, unless otherwise specified

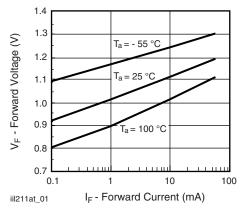


Fig. 3 - Forward Voltage vs. Forward Current

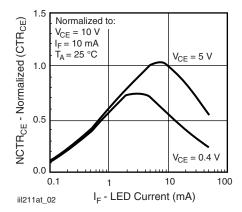


Fig. 4 - Normalized Non-Saturated and Saturated CTR $_{\mbox{\scriptsize CE}}$ vs. LED Current



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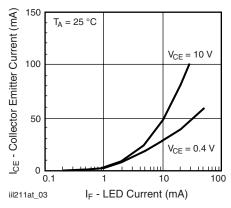


Fig. 5 - Collector Emitter Current vs. LED Current

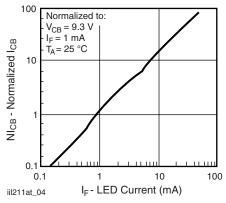


Fig. 6 - Normalized Collector Base Photocurrent vs. LED Current

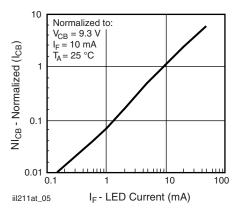


Fig. 7 - Normalized Collector Base Photocurrent vs. LED Current

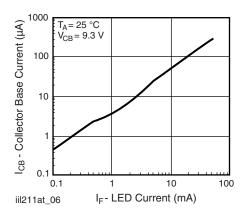


Fig. 8 - Collector Base Photocurrent vs. LED Current

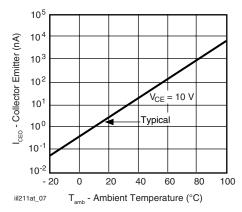


Fig. 9 - Collector Emitter Leakage Current vs. Temperature

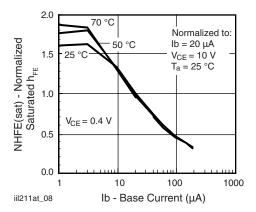
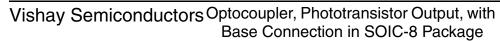


Fig. 10 - Normalized Saturated h_{FE} vs. Base Current and Temperature





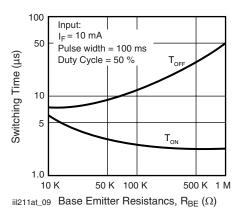


Fig. 11 - Typical Switching Characteristics vs. Base Resistance (Saturated Operation)

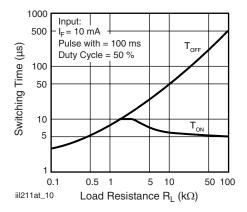
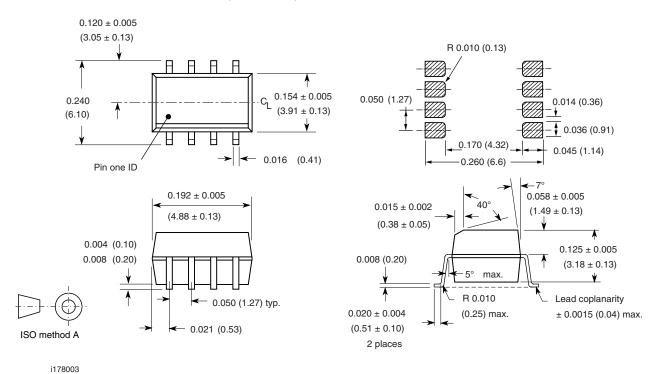


Fig. 12 - Typical Switching Times vs.Load Resistance

PACKAGE DIMENSIONS in inches (millimeters)





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OZONE DEPLETING SUBSTANCES POLICY STATEMENT

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively.
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA.
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

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Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany

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Vishay

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