Single-Supply, 150MHz, 16-Bit Accurate, Ultra-Low Distortion Op Amps

General Description

The MAX4434/MAX4435 single and MAX4436/MAX4437 dual operational amplifiers feature wide bandwidth, 16-bit settling time in 23ns, and low-noise/low-distortion operation. The MAX4434/MAX4436 are compensated for unitygain stability and have a small-signal -3dB bandwidth of 150MHz. The MAX4435/MAX4437 are compensated for closed-loop gains of +5 or greater and have a small-signal, -3dB bandwidth of 150MHz.

The MAX4434–MAX4437 op amps require only 15mA of supply current per amplifier while achieving 115dB open-loop gain. Voltage noise density is a low $2.2nV/\sqrt{Hz}$ and provides 97dB spurious-free dynamic range (SFDR) at 1MHz. These characteristics make these op amps ideal for driving modern, high-speed 14- and 16-bit analog-to-digital converters (ADCs).

These high-speed op amps feature wide-output voltage swings and a high-current output drive up to 65mA. Using a voltage feedback architecture, the MAX4434–MAX4437 meet the requirements of many applications that previously depended on current feedback amplifiers.

The MAX4434/MAX4435 are available in space-saving 5-pin SOT23 packages and the MAX4436/MAX4437 are available in 8-pin $\mu MAX^{I\!\!R}$ packages.

Applications

- High-Speed 14- and 16-Bit ADC Preamplifiers
- Low-Noise Preamplifiers
- IF/RF Amplifiers
- Low-Distortion Active Filters
- High-Performance Receivers
- Precision Instrumentation

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Pin Configurations



Features

- 16-Bit Accurate Settling in 23ns (MAX4435/MAX4437)
- 97dB SFDR at 1MHz, 4VP-P Output
- 2.2nV/√Hz Input Voltage-Noise Density
- 100dB (min) Open-Loop Gain
- 388V/µs Slew Rate (MAX4435/MAX4437)
- 65mA High Output Drive
- Available in Space-Saving Packages
- 5-Pin SOT23 (MAX4434/MAX4435)
- 8-Pin µMAX (MAX4436/MAX4437)

Ordering Information

PART	TEMP RANGE	PIN-PACKAGE
MAX4434EUK-T	-40°C to +85°C	5 SOT23
MAX4434ESA	-40°C to +85°C	8 SO
MAX4435EUK-T	-40°C to +85°C	5 SOT23
MAX4435ESA	-40°C to +85°C	8 SO
MAX4436EUA	-40°C to +85°C	8 µMAX
MAX4436ESA	-40°C to +85°C	8 SO
MAX4437EUA	-40°C to +85°C	8 µMAX
MAX4437ESA	-40°C to +85°C	8 SO

+Denotes lead(Pb)-free/RoHS-compliant package. -Denotes a package containing lead(Pb).

Selector Guide appears at end of data sheet.

Typical Operating Circuit





Single-Supply, 150MHz, 16-Bit Accurate, Ultra-Low Distortion Op Amps

Absolute Maximum Ratings

Supply Voltage (V _{CC} - V _{EE})	+6V
Differential Input Voltage	+2V
Input Voltage Range(V _{CC} + 0.3V) to	(V _{EE} - 0.3V)
Current into Any Input Pin	±25mA
Output Short-Circuit Duration to V _{CC} or V _{EE}	(Note 1)
Continuous Power Dissipation $(T_A = +70^{\circ}C)$	
5-Pin SOT23 (derate 7.1mW/°C above +70°C)	571mW

8-Pin SO (derate 5.88mW/°C above +70°C).	471mW
8-Pin µMAX (derate 4.5mW/°C above +70°C	;) 330mW
Operating Temperature Range	40°C to +85°C
Junction Temperature	+150°C
Storage Temperature Range	-65°C to +150°C
Lead Temperature (soldering, 10s)	+300°C

Note 1: The MAX4434–MAX4437 are not protected for output short-circuit conditions.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

DC Electrical Characteristics

(V_{CC} = +5V, V_{EE} = 0V, R_L = ∞ to V_{CC}/2, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 2)

PARAMETER	SYMBOL	C	ONDITIONS	MIN	TYP	MAX	UNITS	
Input Common-Mode Voltage Range	V _{CM}	Guaranteed by	CMRR test	V _{EE}		V _{CC} - 1	V	
Input Offset Voltage	V _{OS}				1		mV	
Input Offset Voltage Temperature Coefficient	TCV _{OS}				4		µV/°C	
Input Offset Voltage Matching		MAX4436/MAX	4437		0.25		mV	
Input Bias Current	Ι _Β				14	22	μA	
Input Offset Current	I _{OS}				1	5	μA	
Input Posistanco	P	Differential Mod	e -10mV ≤ V _{IN} ≤ +10mV		1		kΩ	
		Common Mode $0 \le V_{CM} \le (V_{CC} - 1V)$			1.7		MΩ	
Common-Mode Rejection Ratio	CMRR	$V_{EE} \le V_{CM} \le (V_{CM} < (V_{CM} \le (V_{CM} < (V_{CM$	/ _{CC} - 1V)	75	100		dB	
	Δ	$ \begin{array}{l} (V_{EE} + 0.25) \leq V_{OUT} \leq (V_{CC} \mbox{-}0.25), \\ R_{L} = 10 \mathrm{k} \Omega \end{array} $		100	115		dD	
Open-Loop Gain	$(V_{EE} + 0.5) \le V_{C}$ R _L = 500Ω		$OUT \leq (V_{CC} - 0.5),$	96	110			
Output Voltage Suing	V	D = 10k0	V _{CC} - V _{OH}		65	200		
Output voltage Swing	^V OUT	$R_{L} = 10k\Omega$	V _{OL} - V _{EE}		15	70	mv	
Quitaut Quimant		$R_L = 20\Omega$ to	Sinking	40	65			
Output Current	^I OUT	Ground	Sourcing	35	60		ma	
Output Short-Circuit Current	I _{SC}	Sinking or sourcing			±70		mA	
DC Power-Supply Rejection Ratio	PSRR	$V_{\rm CC}$ = +4.5V to +5.5V		85	110		dB	
Operating Supply Voltage	VS	Guaranteed by	PSRR test	+4.5		+5.5	V	
Quiescent Supply Current (Per Amplifier)	I _S				15	18	mA	

Note 2: All devices are 100% production tested at +25°C. Specifications over temperature limits are guaranteed by design.

Single-Supply, 150MHz, 16-Bit Accurate, Ultra-Low Distortion Op Amps

AC Electrical Characteristics

(V_{CC} = +5V, V_{EE} = 0V, V_{CM} = V_{CC}/2, R_L = 500 Ω , A_{VCL} = +1, and T_A = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS	
Small-Signal -3dB		V _{OUT} = 20mVp-p MAX4434/MAX4436		150			
Bandwidth	BVVSS	V _{OUT} = 20mVp-p MAX4435/MAX4437 (A _{VCL} = +5)		150			
Large-Signal -3dB	DW	V _{OUT} = 2Vp-p MAX4434/MAX4436		28		NAL I-	
Bandwidth	BVVLS	V _{OUT} = 4Vp-p MAX4435/MAX4437 (A _{VCL} = +5)		25		MHZ	
Small-Signal 0.1dB		V _{OUT} = 20mVp-p MAX4434/MAX4436		80			
Gain Flatness	BVV0.1dBSS	V _{OUT} = 20mVp-p MAX4435/MAX4437 (A _{VCL} = +5)		80		MHZ	
Large-Signal 0.1dB		V _{OUT} = 2Vp-p MAX4434/MAX4436		15		NAL I-	
Gain Flatness	BW _{0.1dBLS}	V _{OUT} = 4Vp-p MAX4435/MAX4437 (A _{VCL} = +5)		20		MHZ	
Claux Data	SR	V _{OUT} = 2V step MAX4434/MAX4436	133				
Slew Rate		V _{OUT} = 4V step MAX4435/MAX4437 (A _{VCL} = +5)		388		- ν/μs	
	t _R , t _F	V _{OUT} = 2V step MAX4434/MAX4436	17				
Rise/Fail Time		V _{OUT} = 4V step MAX4435/MAX4437 (A _{VCL} = +5)		10		- ns	
		V _{OUT} = 1.5V to 3.5V step MAX4434/MAX4436		35			
Settling Time to 16-Bit (0.0015%)	^t S0.0015%	V _{OUT} = 1.5V to 3.5V step MAX4435/MAX4437 (A _{VCL} = +5)		23		ns	
		V _{OUT} = 1V to 4V step MAX4434/MAX4436		42			
Output "Glitch" Settling to 16-Bit (0.0015%)		5pF load, C _L charged from 1V to 4V		41		ns	
Output Overload Recovery Time		50% overdrive, settling to 10% accuracy		100		ns	
AC Common-Mode Rejection Ratio	CMRR	f _C = 100kHz		-92		dB	

Single-Supply, 150MHz, 16-Bit Accurate, Ultra-Low Distortion Op Amps

AC Electrical Characteristics (continued)

(V_{CC} = +5V, V_{EE} = 0V, V_{CM} = V_{CC}/2, R_L = 500 Ω , A_{VCL} = +1, and T_A = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	cc	ONDITIONS	MIN T	P MAX	UNITS
AC Power-Supply Rejection Ratio	PSRR	f _C = 100kHz	f _C = 100kHz)1	dB
		$V_{OUT} = 2V_{p-p}$	f _C = 100kHz	-9	7	
		V _{CC} /2	f _C = 1MHz	-9	8	
Spurious-Free		$V_{OUT} = 3V_{p-p}$	f _C = 100kHz	-1:	30	dDa
Dynamic Range	SFDR	V _{CC} /2	f _C = 1MHz	-9	9	- dBc
		$V_{OUT} = 4V_{p-p}$ centered at $V_{CC}/2$	f _C = 100kHz	-1'	12	
			f _C = 1MHz	-9	7	
Input Noise Voltage Density	e _n	f _C = 100kHz		2.	2	nV/√Hz
Input Noise Current Density	i _n	f _C = 100kHz		2.	0	pA/√Hz
Input Capacitance	C _{IN}			2.	3	pF
Maximum Capacitive Load Without Sustained Oscillations				3	0	pF
Output Impedance	Z _{OUT}	f _C = 1MHz		0.0)5	Ω
Crosstalk		f _C = 1MHz, MAX	4436/MAX4437	-8	0	dB

Typical Operating Characteristics

(V_{CC} = +5V, V_{EE} = 0V, R_L = 500 Ω , C_L = 0, T_A = +25°C, unless otherwise noted.)



Single-Supply, 150MHz, 16-Bit Accurate, Ultra-Low Distortion Op Amps

Typical Operating Characteristics (continued)

(V_{CC} = +5V, V_{EE} = 0V, R_L = 500 Ω , C_L = 0, T_A = +25°C, unless otherwise noted.)



20ns/div

20ns/div

20ns/div

Single-Supply, 150MHz, 16-Bit Accurate, Ultra-Low Distortion Op Amps

Typical Operating Characteristics (continued)

(V_{CC} = +5V, V_{EE} = 0V, R_L = 500 Ω , C_L = 0, T_A = +25°C, unless otherwise noted.)



Single-Supply, 150MHz, 16-Bit Accurate, Ultra-Low Distortion Op Amps

Typical Operating Characteristics (continued)

(V_{CC} = +5V, V_{EE} = 0V, R_L = 500 Ω , C_L = 0, T_A = +25°C, unless otherwise noted.)



Single-Supply, 150MHz, 16-Bit Accurate, Ultra-Low Distortion Op Amps

Typical Operating Characteristics (continued)

 $(V_{CC} = +5V, V_{EE} = 0V, R_L = 500\Omega, C_L = 0, T_A = +25^{\circ}C$, unless otherwise noted.)





Pin Description

	PIN		
MAX443	4/MAX4435	NAME FUNCTION	
SOT23	SO		
1	6	OUT	Output
2	4	V _{EE}	Ground
3	3	IN+	Noninverting Input
4	2	IN-	Inverting Input
5	7	V _{CC}	Positive Power Supply
—	1, 5, 8	N.C.	No Connection. Not internally connected.

PIN		
MAX4436/MAX4437	NAME	FUNCTION
SO/μΜΑΧ		
1	OUTA	Amplifier A Output
2	INA-	Amplifier A Inverting Input
3	INA+	Amplifier A Noninverting Input
4	V _{EE}	Ground
5	INB+	Amplifier A Noninverting Input
6	INB-	Amplifier A Inverting Input
7	OUTB	Amplifier A Output
8	V _{CC}	Positive Power Supply

Detailed Description

The MAX4434–MAX4437 are wide-bandwidth, ultra-low -distortion, voltage-feedback amplifiers. The MAX4434/ MAX4436 are internally compensated for unity gain. The MAX4435/MAX4437 are internally compensated for gains of +5V/V or greater.

These amplifiers have ultra-fast 35ns (MAX4434/ MAX4436) 16-bit settling times, -97dB SFDR at 1MHz, and 4Vp-p output swing with minimum 115dB open-loop gain.

High-Speed ADC Input Driver Application

The MAX4434–MAX4437 op amps are ideal for drivinghigh-speed 14- to 16-bit ADCs. In most cases, these ADCs operate with a charge balance scheme, with capacitive loads internally switched on and off from the input. The driver used must withstand these changing capacitive loads while holding the signal amplitude stability consistent with the ADC's resolution and, at the same time, have a frequency response compatible with the sampling speed of the ADC (Figure 1).

Inverting and Noninverting Configurations

The circuits typically used for the inverting and noninverting configurations of the MAX4434–MAX4437 are shown in Figure 2a and Figure 2b. The minimum uncondition-



Figure 1. Typical Application Circuit



Figure 2a. Noninverting Configuration

Single-Supply, 150MHz, 16-Bit Accurate, Ultra-Low Distortion Op Amps

ally stable gain values are 1 for the MAX4434/MAX4436 and 5 for the MAX4435/MAX4437. Use care in selecting the value for the resistor marked R_S in both circuits. From dynamic stability considerations (based on the part's frequency response and the input capacitance of the MAX4434–MAX4437), the maximum recommended value for R_S is 500 Ω . In general, lower R_S values will yield a higher bandwidth and better dynamic stability, at the cost of higher power consumption, higher power dissipation in the IC, and reduced output drive availability For a minimum R_S value, take into consideration that the current indicated as I_F is supplied by the output stage and must be discounted from the maximum output current to calculate the maximum current available to the load. I_F can be found using the following equation:

$I_F = V_{IN(MAX)}/R_S$

If DC thermal stability is an important design concern, the Thevenin resistance seen by both inputs at DC must be balanced. This includes the resistance of the signal source and termination resistors if the amplifier signal input is fed from a transmission line. The capacitance associated with the feedback resistors must also be considered as a possible limitation to the available bandwidth or to the dynamic stability. Only resistors with small parallel capacitance specifications should be considered.

Applications Information

Layout and Power-Supply Bypassing

The MAX4434–MAX4437 have wide bandwidth and consequently require careful board layout. To realize the full AC performance of these high-speed amplifiers, pay careful attention to power-supply bypassing and board layout. The PC board should have a large low-impedance ground plane that is as free of voids as possible. Do not use commercial breadboards. Keep signal lines as short and straight as possible. Observe high-frequency bypass-



Figure 2b. Inverting Configuration

Single-Supply, 150MHz, 16-Bit Accurate, Ultra-Low Distortion Op Amps



Figure 3. Capacitive-Load Driving Circuit

ing techniques to maintain the amplifier's accuracy and stability. In general, use sur-face-mount components since they have shorter bodies and lower parasitic reactance. This will result in improved performance over through-hole components. The bypass capacitors should include 1nF and/or 0.1 μ F surface-mount ceramic capacitors between V_{CC} and the ground plane, located as close to the package as possible. Place a 10 μ F tantalum capacitor at the power supply's point of entry to the PC board to ensure the integrity of the incoming supplies. Input termination resistors and output back-termination resistors, if used, should be surface-mount types and should be placed as close to the IC pins as possible.

Driving Capacitive Loads

The MAX4434–MAX4437 can drive capacitive loads. However, excessive capacitive loads may cause ringing or instability at the output as phase margin is reduced. Adding a small isolation resistor in series with the output capacitive load helps reduce the ringing but slightly increases gain error (see <u>Typical Operating</u> <u>Characteristics</u> and Figure 3).

Selector Guide

PART	AMPS	MIN GAIN STABLE (V/V)	BW (MHz)	SETTLING TIME TO 0.0015% (ns)
MAX4434	1	+1	150	35
MAX4435	1	+5	150	23
MAX4436	2	+1	150	35
MAX4437	2	+5	150	23

Pin Configurations (continued)



Chip Information

MAX4434/MAX4435 TRANSISTOR COUNT: 141 MAX4436/MAX4437 TRANSISTOR COUNT: 318

Single-Supply, 150MHz, 16-Bit Accurate, Ultra-Low Distortion Op Amps

Package Information

For the latest package outline information and land patterns (footprints), go to <u>www.maximintegrated.com/packages</u>. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	DOCUMENT NO.	LAND PATTERN NO.
5 SOT23	U5-1	<u>21-0057</u>	<u>90-0174</u>
8 SO	S8-2	<u>21-0041</u>	<u>90-0096</u>
8 µMAX	U8-1	<u>21-0036</u>	<u>90-0092</u>

Single-Supply, 150MHz, 16-Bit Accurate, Ultra-Low Distortion Op Amps

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	10/01	Initial release	—
1	12/08	Added automotive part number	1
2	4/15	Removed automotive reference from data sheet	1

For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim Integrated's website at www.maximintegrated.com.

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