PQ7DV5

Variable Output Type, High Output Current (5A) Type Low Power-loss Voltage Regulators

Features

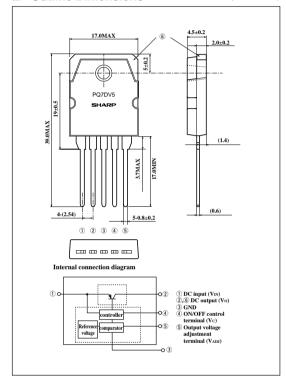
- TO-3P package
- Low power-loss (Dropout voltage:MAX. 0.5V at Io=5A)
- Variable output type (1.5V to 7V)
- Minimum input voltage: 3.0V
- High output current type (5A)
- Reference voltage precision: ±2.0%
- Built-in ON/OFF control function
- Built-in overcurrent protection, overheat protection function

Applications

 Power supplies for various electronic equipment such as personal computers

Outline Dimensions

(Unit:mm)



■ Absolute Mximum Ratings

(Ta=25°C)

Parameter		Rating	Unit	
*1 Input voltage	Vin	10	V	
*1 ON/OFF control terminal voltage	Vc	10	V	
*1 Output adjustment terminal voltage	VADJ	5	V	
Output current	Io	5.0	A	
Power dissipation (No heat sink)	PD1	2,2	W	
Power dissipation (With infinite heat sink)	P _{D2}	60	W	
*2 Junction temperature	Tj	150	.с	
Operating temperature	Topr	-20 to +80	,C	
Storage temperature	Tstg	-40 to +150	.с	
Soldering temperature	Tsol	260 (For 10s)	,C	

^{*1} All are open except GND and applicable terminals.

^{*2} Overheat protection may operate at 125<=Tj<=150*C.

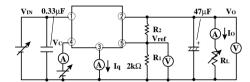
■ Electrical Characteristics

(Unless otherwise specified, conditions shall be V_{IN}=5V, Io=2.5A, Vo=3V [R₁=2kΩ] T_a=25 °C)

Parameter	Symbol	Conditions	NIN.	TYP.	MAX.	Unit
Input voltage	Vin	-	3	-	10	V
Output voltage	Vo	-	1.5	-	7	V
Reference voltage	V_{ref}	-	1.225	1.25	1.275	V
Load regulation	RegL	Io=5mA to 5.0A	-	0.5	2.0	%
Line regulation	RegI	V _{IN} =4 to 10V	-	0.5	2.5	%
Temperature coefficient of reference voltage	TcVo	T _j =0 to 125°C	-	±0.01	-	%/*C
Ripple rejection	RR	-	45	55	-	dB
Dropout voltage	Vi-o	V _{IN} =3V, Io=5A	-	-	0.5	V
*3 ON-state voltage for control	Vc (on)	-	2.0	-	-	V
ON-state current for control	Ic (on)	Vc=2.7V	-	-	20	μ A
OFF-state voltage for control	V _C (OFF)	-	-	-	0.8	V
OFF-state current for control	Ic (off)	Vc=0.4V	-	-	- 0.4	mA
Quiescent current	$\mathbf{I}_{\mathbf{q}}$	$I_0=0A$	-	-	17	mA

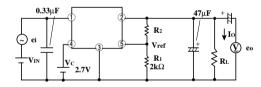
^{*3} In case of opening control terminal 4, output voltage turns on.

Fig.1 Test Circuit



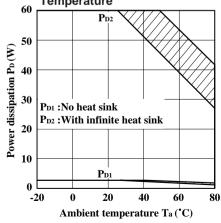
 $V_0 = V_{ref} \times (1 + R_2/R_1)$ = 1.25 \times (1 + R_2/R_1) [R_1 = 2k\Omega, V_{ref} = 1.25V]

Fig.2 Test Circuit for Ripple Rejection



f=120Hz (sine wave) ei=0.5Vrms V_{IN} =5V V_0 =3V (R_1 =2k Ω) Io=0.5A RR=20 log (ei/eo)

Fig.3 Power Dissipation vs. Ambient Temperature



Note) Oblique line portion:Overheat protection may operate in this area.

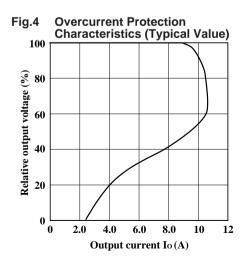


Fig.5 Reference Voltage Deviation vs. Junction Temperature

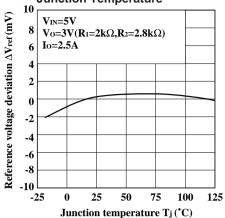


Fig.7 Circuit Operating Current vs. Input Voltage

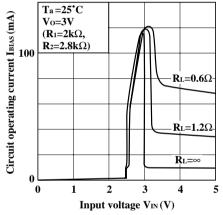


Fig.9 Quiescent Current vs. Junction Temperature

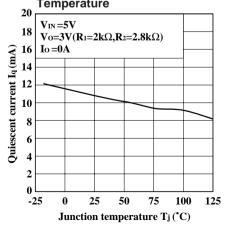


Fig.6 Output Voltage vs. Input Voltage

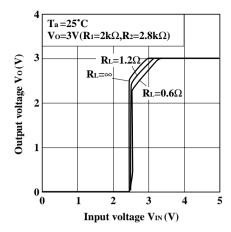


Fig.8 Dropout Voltage vs. Junction Temperature

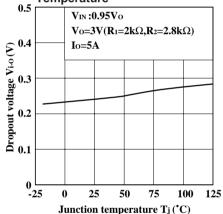
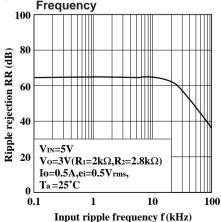
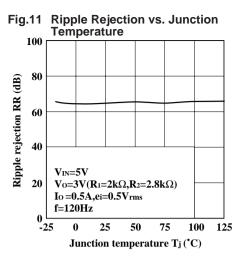
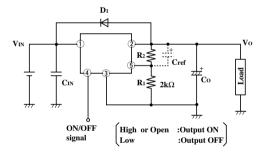


Fig.10 Ripple Rejection vs. Input Ripple





Standard Connection



D1 : This device is necessary to protect the element from damage when reverse voltage may be applied to the regulator in case of input short-circuiting.

Cref : This device is necessary when it is required to enhance the ripple rejection or to delay the output start-up time*. Otherwise, it is not necessary.

(Care must be taken since Cref may raise the gain, facilitating oscillation.)

* The output start-up time proportional to Cref X R2.

CIN, Co: Be sure to mount the devices CIN and Co as close to the device terminal as possible so as to prevent oscillation.

The standard specification of C_{IN}= 0.33µF ,Co= 47µF, respectively. However, adjust them as necessary after checking.

R1, R2: These devices are necessary to set the output voltage. The output voltage Vo is given by the following formula:

 $V_0 = V_{ref} X (1 + R_2/R_1)$

(V_{ref} is 1.25V TYP)

The standard value of R₁ is 2Ω . But value up to $10k\Omega$.

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